



03C0
#3

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)
FRANCIS ET AL.)
)
Serial No. 10/008,586)
)
Confirmation No. 2454)
)
Filing Date: NOVEMBER 5, 2001)
)
For: DEVICE AND METHOD FOR)
SELECTIVELY POWERING DOWN)
INTEGRATED CIRCUIT BLOCKS)
WITHIN A SYSTEM ON CHIP)

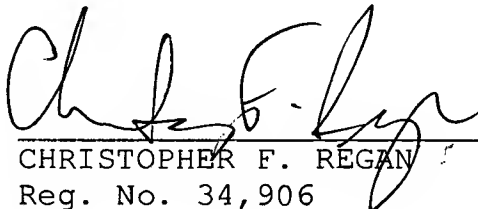
TRANSMITTAL OF FORMAL DRAWINGS

Director, U.S. Patent and Trademark Office
Washington, D.C. 20231

Sir:

Enclosed are five (5) sheets of formal drawings to be
filed in the above-identified patent application.

Respectfully submitted,


CHRISTOPHER F. REGAN

Reg. No. 34,906
Allen, Dyer, Doppelt, Milbrath
& Gilchrist, P.A.
255 S. Orange Ave., Suite 1401
P. O. Box 3791
Orlando, Florida 32802
(407) 841-2330

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being
deposited with the United States Postal Service as first class
mail in an envelope addressed to: DIRECTOR, U.S. PATENT AND
TRADEMARK OFFICE, WASHINGTON, D.C. 20231, on this 31st day of
January, 2002.

